

IN THE CLAIMS

Please amend the claims as follows:

1. (AMENDED) A time-shifted video method comprising:
in a real-time mode, delivering real-time video frames
for display in response to a digital input signal,

in a time-shifted mode, delivering time-shifted video
5 frames for display in response to a digital input signal, the time-
shifted video frames being delayed relative to the real-time video
frames, and

pausing a real-time frame during a transition from the
real-time mode to the time-shifted mode.

2. (AMENDED) The method of claim 1, wherein the
transition is between the paused real-time frame and a time-shifted
version of the paused real-time frame.

3. (AMENDED) The method of claim 1, further comprising
providing trick functions during the time-shifted mode.

4. (AMENDED) The method of claim 1, wherein the
transition mode is triggered by a command of a viewer or an event
generated by software.

5. (AMENDED) The method of claim 1, wherein the real-time video frames are derived from uncompressed video.

6. (AMENDED) The method of claim 5, wherein the real-time video frames are provided from an input frame buffer.

7. (AMENDED) The method of claim 1, wherein the real-time video frames are derived from input compressed video.

8. (AMENDED) The method of claim 7, wherein the real-time frames are provided from a decoder that decompresses the input compressed video.

9. (AMENDED) The method of claim 1, wherein the real-time mode, the time-shifted mode, and the transition are provided by a single codec chip.

10. (AMENDED) The method of claim 8, wherein the compressed video comprises MPEG video.

11. (AMENDED) The method of claim 1, wherein information is stored identifying the paused frame, and before the time-shifted

mode occurs, a predetermined frame or a next frame after the predetermined frame is queued up.

13. (AMENDED) The apparatus of claim 22, further comprising a real-time processing path including a real-time decoder and the time-shifted decoder that deliver real-time video to an output based on the digital video input.

14. (AMENDED) The apparatus according to claim 13, wherein the real-time decoder and the time-shifted decoder are provided in a single codec.

15. (AMENDED) The apparatus of claim 23, having a processing path for said real-time mode and a processing path for said time-shifted mode.

16. (AMENDED) The apparatus of claim 21, wherein an encoder and the time-shifted decoder are provided in a single codec.

17. (AMENDED) The apparatus of claim 21, wherein processing paths include buffers in a common memory.

18. (AMENDED) The apparatus of claim 23, wherein the apparatus comprises a set-top box.

19. (AMENDED) The apparatus of claim 23, wherein the apparatus is configured to present signals viewable by an analog television.

20. (TWICE AMENDED) A set-top box comprising:

a real-time decoder configured to (i) generate a first output in response to a compressed digital video input signal and (ii) pause a frame of said first output during a transition from a first mode to a second mode,

a frame storage system configured to store said compressed digital video signal separately from said real-time decoder,

a time-shifted decoder (i) coupled to the frame storage system and (ii) configured to generate a second output in response to said stored compressed digital video signal, and

a controller configured to generate a command configured to control presenting (i) said first output when in said first mode and (ii) said second output when in said second mode, wherein said first output and said second output are viewable by a display device.

21. (TWICE AMENDED) A television receiver comprising:

a frame buffer configured to (i) present an output in response to an uncompressed video signal and (ii) pause a frame of said output during a transition from a first mode to a second mode,

5 a frame storage system configured to store said uncompressed video signal separately from said frame buffer,

a time-shifted decoder configured to generate a second output in response to said stored uncompressed video signal, and

10 a controller configured to generate a command configured to control presenting (i) said first output when in said first mode and (ii) said second output when in said second mode, wherein said first output and said second output are viewable by a display device.

22. (TWICE AMENDED) A set-top box comprising:

a controller configured to receive a command and a compressed digital video input,

5 a frame buffer configured to (i) generate a first output in response to the compressed digital video input and (ii) pause a frame of said first output during a transition from a first mode to a second mode,

a frame storage system coupled to the controller, and

10 a time-shifted decoder coupled to the frame storage system and the controller configured to generate a second output in response to (i) said compressed digital video input, and (ii) said command;

15 wherein the controller is configured to generate a second command configured to control presenting (i) said first output when in said first mode and (ii) said second output when in said second mode, wherein said first output and said second output are viewable by an analog display device.

23. (AMENDED) An apparatus comprising:

5 a frame buffer configured to (i) generate a first signal in response to a digital input signal and (ii) pause a real-time frame during a transition from a real-time mode to a time-shifted mode;

an encoder configured to generate a second signal in response to said digital input signal, wherein said second signal is (i) stored in a buffer and (ii) retrieved separate from being stored; and

10 a controller configured to present an output signal comprising (i) said first signal when in said real-time mode and (ii) said retrieved second signal when in said time-shifted mode.

24. The method according to claim 2, wherein said transition is seamless to a viewer.